



27/04/2024

Delay on:	D3.2
Delivery Date in Annex 1:	M39
Expected Delivery Date:	M45

Justification for delay

After signing the grant agreement but before the start of work on the Picosecond TLU (D3.2) a key member of personnel became unavailable. This has caused considerable delay. Recently we have been able to reallocate effort and work has started on the Picosecond TLU at a higher level of staff effort than originally planned. However, there will still be a delay. The current schedule is as follows:

June/July 2024 – Hardware prototypes of analogue front end under test

October 2024 – Prototype of full hardware ready for test

January 2025 – Firmware and integration with EUDAQ software ready.